

FIG 1

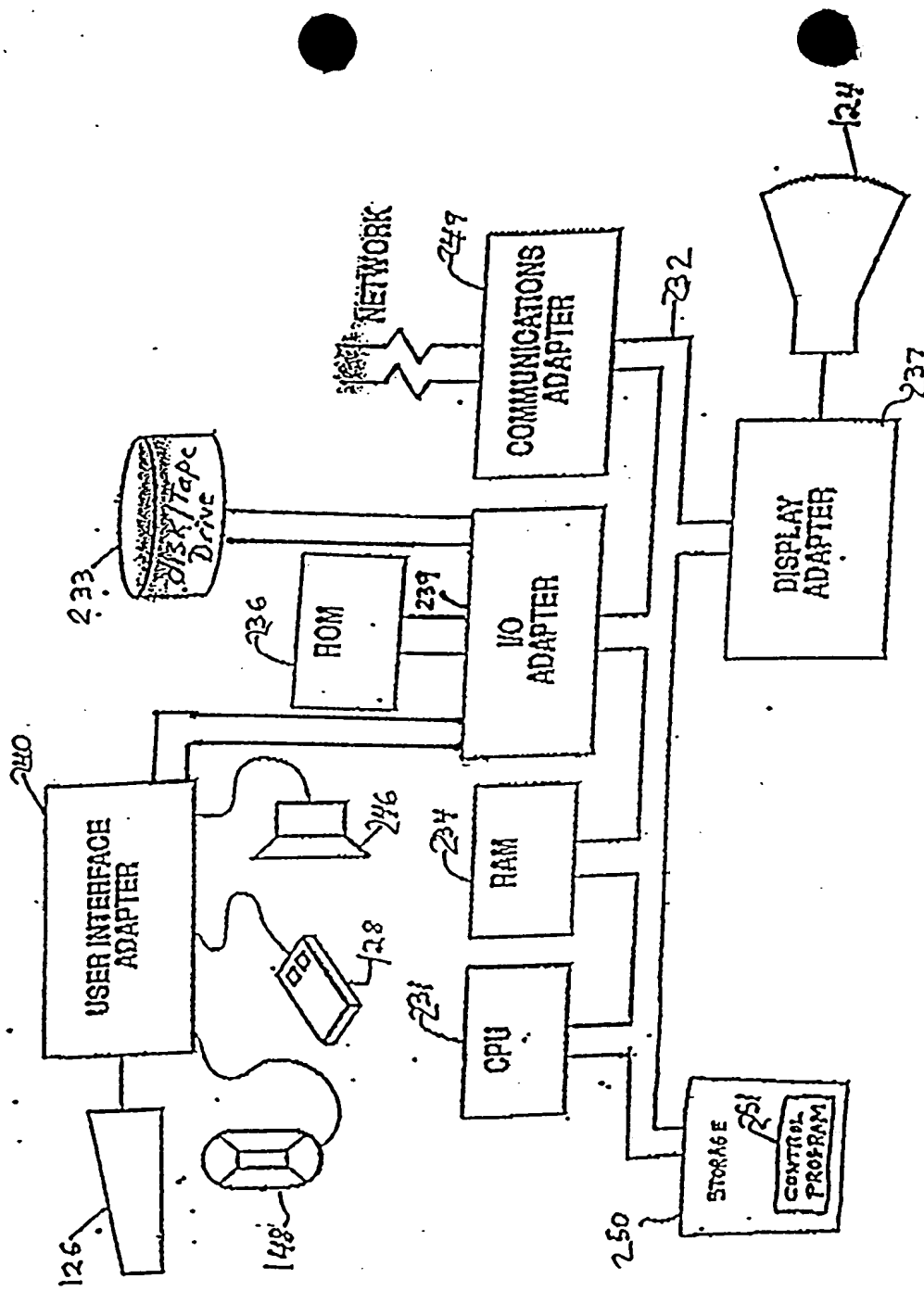


FIG. 2

120

FIG. 3 is a block diagram of a system architecture. The system includes a CPU 301, a Local Frame Buffer 312, an AGP-Enabled Graphics Controller 300, an AGP-Enabled Northbridge 304, a 100 MHz Bridge Clock 302, a 100 MHz Memory Clock 303, and System Memory 316. The CPU 301 is connected to the Local Frame Buffer 312 and the AGP-Enabled Graphics Controller 300. The Local Frame Buffer 312 is connected to the AGP-Enabled Graphics Controller 300. The AGP-Enabled Graphics Controller 300 is connected to the AGP-Enabled Northbridge 304. The AGP-Enabled Northbridge 304 is connected to the 100 MHz Bridge Clock 302. The 100 MHz Bridge Clock 302 is connected to the 100 MHz Memory Clock 303. The 100 MHz Memory Clock 303 is connected to the System Memory 316. The System Memory 316 is connected to the AGP-Enabled Northbridge 304. The AGP-Enabled Northbridge 304 is also connected to a Southbridge 322, which is connected to a Network Card 323. The Southbridge 322 is also connected to three PCI devices: PCI Device 320, PCI Device 321, and PCI Device 324.

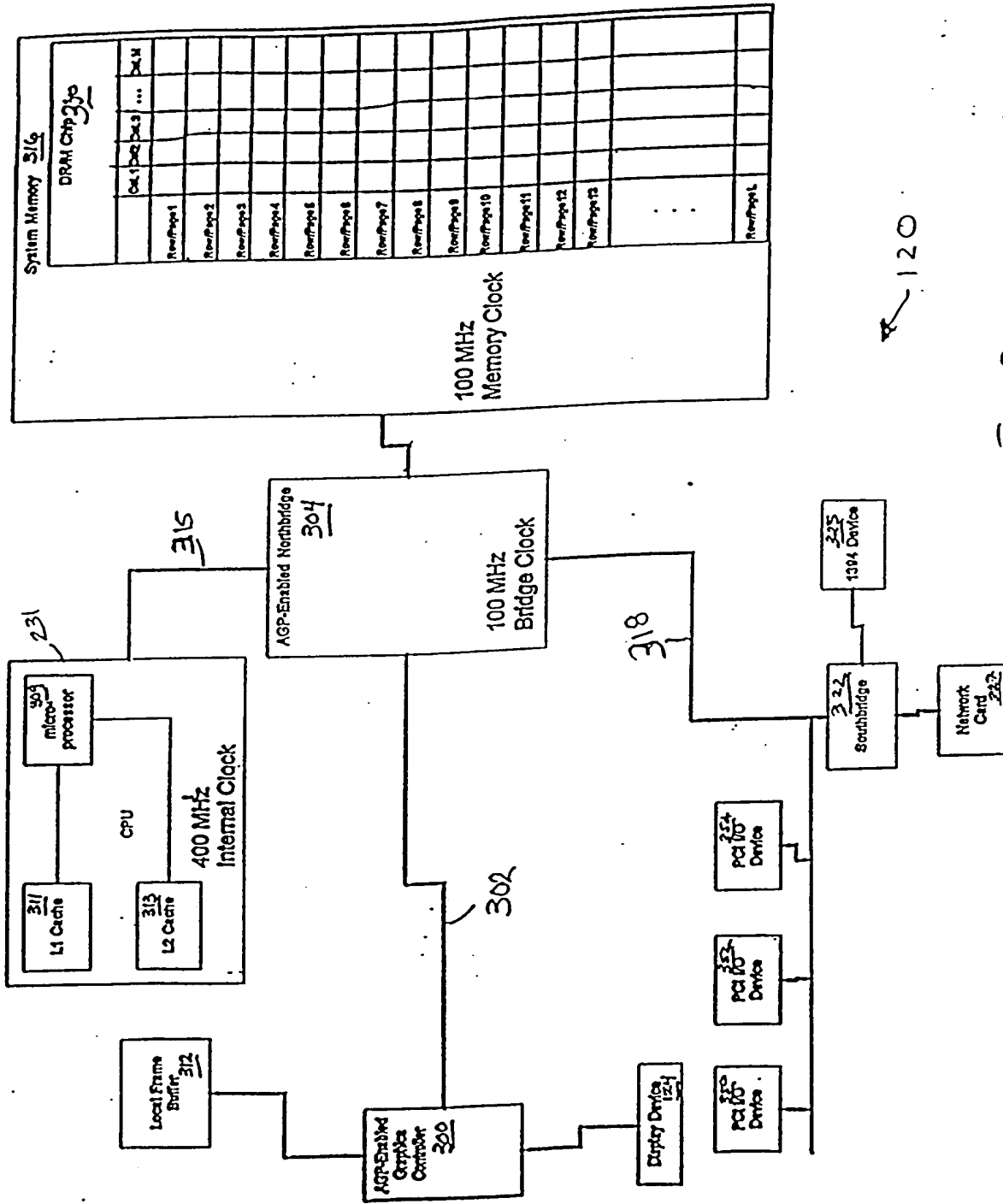


FIG. 3

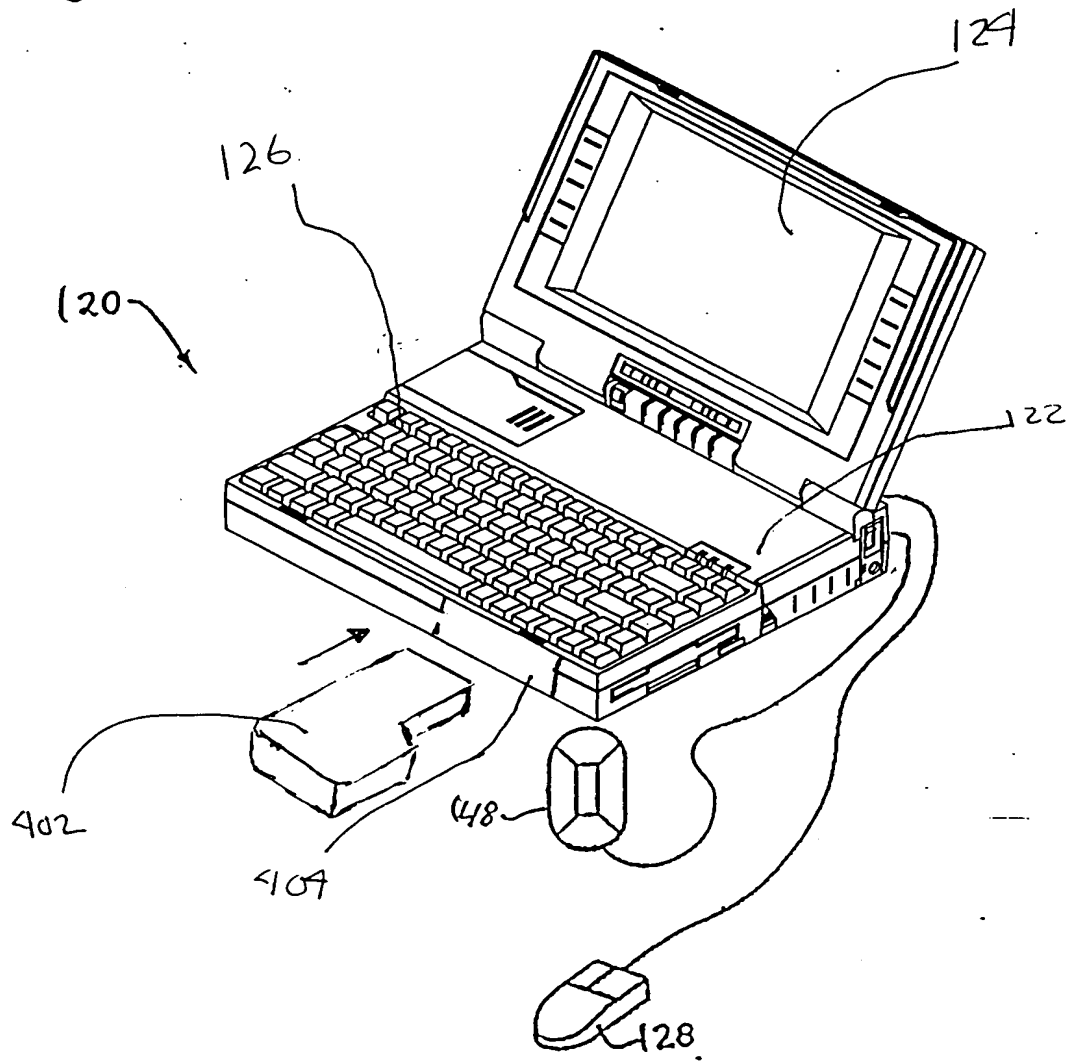


FIG 4

FIG. 5A is a perspective view of a device 100 in an open position. The device 100 includes a housing 102 and a display 104. The housing 102 includes a top surface 106 and a bottom surface 108. The display 104 is mounted to the top surface 106 of the housing 102. The device 100 is shown in a perspective view from the front and slightly to the right. The housing 102 is a rectangular frame with a top surface 106 and a bottom surface 108. The display 104 is a rectangular panel mounted to the top surface 106. The device 100 is shown in an open position, with the display 104 tilted upwards. The housing 102 includes a hinge 110 at the bottom edge of the display 104. The device 100 is shown in a perspective view from the front and slightly to the right. The housing 102 is a rectangular frame with a top surface 106 and a bottom surface 108. The display 104 is a rectangular panel mounted to the top surface 106. The device 100 is shown in an open position, with the display 104 tilted upwards. The housing 102 includes a hinge 110 at the bottom edge of the display 104.

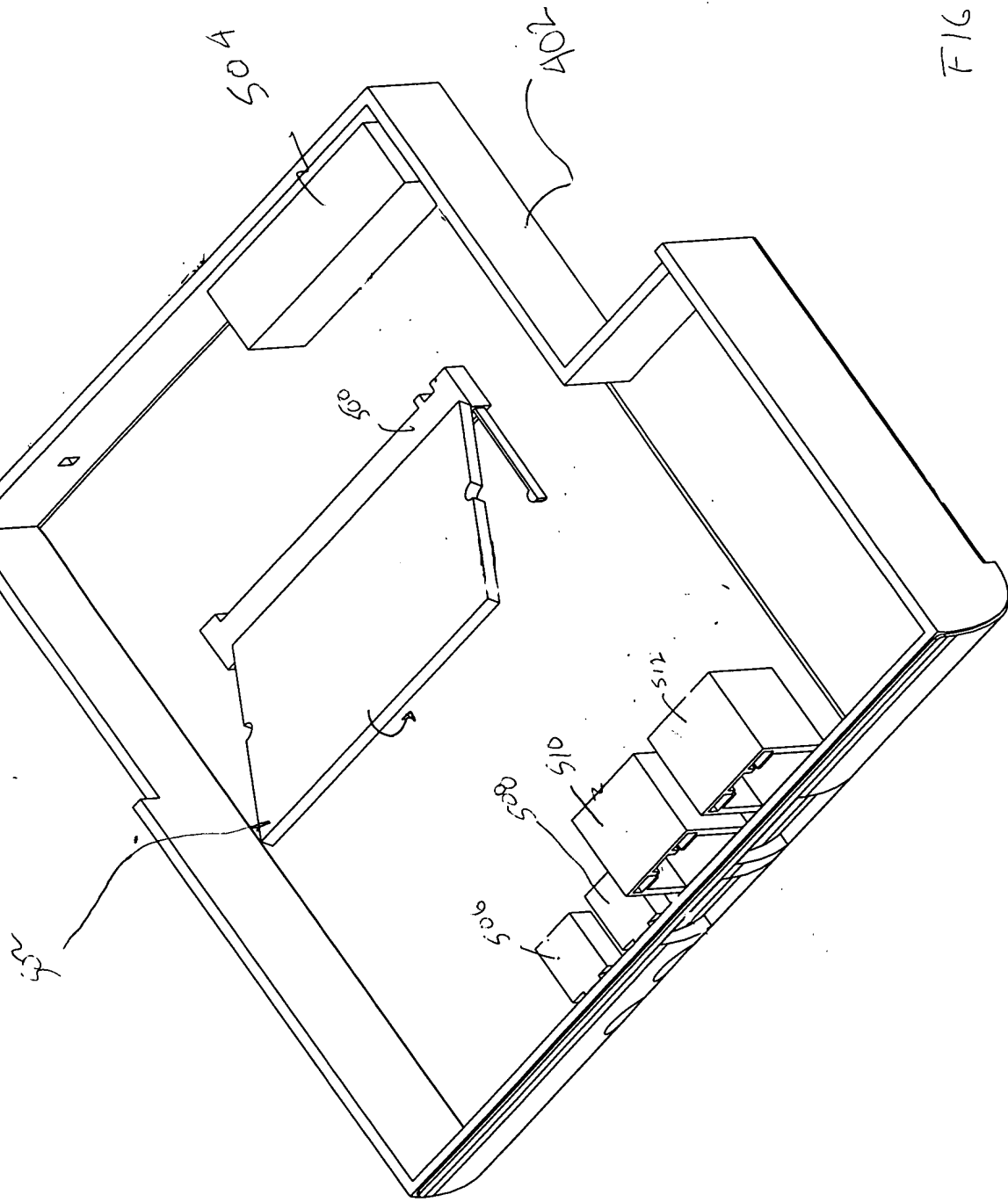


FIG. 5A

FIG. 5A is a perspective view of the device 100 in an open position, showing the interior of the housing 102. The device 100 includes a base 104, a side wall 106, and a front wall 108. The base 104 is formed by a plurality of rectangular blocks 110, 112, and 114. The side wall 106 is formed by a plurality of rectangular blocks 116, 118, and 120. The front wall 108 is formed by a plurality of rectangular blocks 122, 124, and 126. The device 100 is shown in an open position, with the front wall 108 being hinged to the side wall 106. The interior of the housing 102 is shown, and the device 100 is shown in a perspective view.

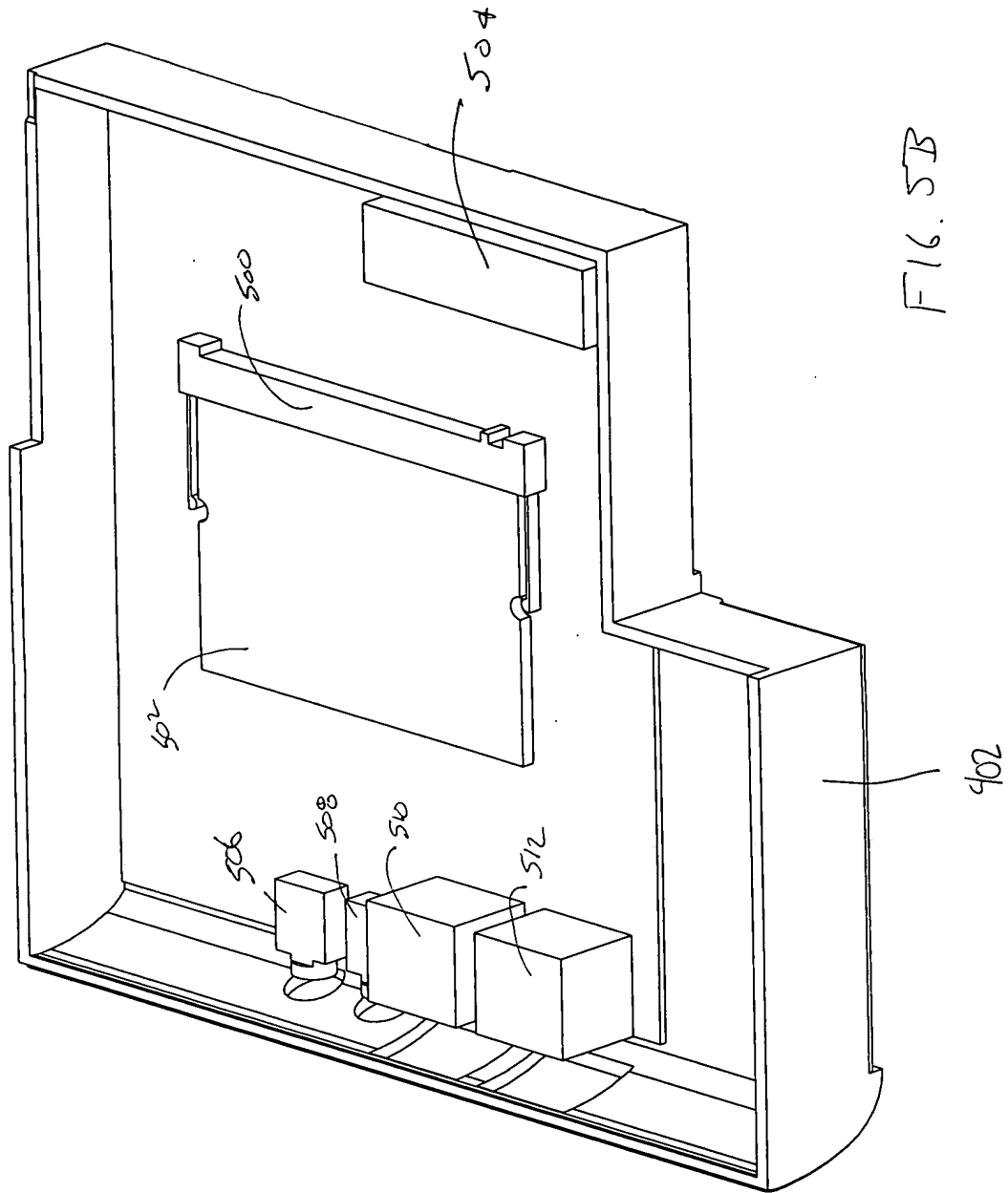


FIG. 5B

